

Docket No.: GR 98 P 2561 P

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By: Wm. S. M.

Date: April 26, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerald Deboy et al.
Applic. No. : 09/804,325
Filed : March 12, 2001
: Switch Mode Power Supply With Reduced Switching Losses

Wm. S. M.
APR 26 2001



INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,216,275 (Chen), dated June 1, 1993;

United States Patent No. 5,438,215 (Tihanyi), dated August 1, 1995;

German Published, Non-Prosecuted Patent Application DE 197 02 102 A1 (Fujihira), dated July 24, 1997, semiconductor device and method for producing the semiconductor device;

International Publication WO 97/29518 (Tihanyi et al.), dated August 14, 1997.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,


For Applicants



WERNER H. STEMER
REG. NO. 34,956

Date: April 26, 2001

Lerner And Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

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